

Please add the following new Claims:

1 w-11. A UART for providing single channel
2 communications, comprising:
3 a transmitter for transmitting signals on a
4 single communications channel;
5 a receiver for receiving signals on the single
6 communications channel;
7 processing means for indicating whether the UART
8 is operating in a full duplex mode of operation; and
9 receiver control circuitry for disabling and
10 enabling the receiver in response to the indication of a
11 full duplex mode of operation by the processing means.

1 2 1 w-12. The UART of Claim 11 further comprising means for
2 storing data to be transmitted by the transmitter, the
3 means for storing generating a signal indicating when data
4 for transmission is contained therein.

1 2 1 2 13. The UART of Claim 12 wherein the receiver control
2 circuitry further disables and enables the receiver in
3 response to the signal indicating data for transmission in
4 the means for storing.

1 4 1 2 14. The UART of Claim 12 wherein the means for
2 storing further comprises:

3 a data store for queuing data to be transmitted
4 by the transmitter; and
5 a FIFO for passing data between the data store
6 and the transmitter.

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1 15. The UART of Claim 14 wherein the receiver control
2 circuitry disables and enables the receiver in response to
3 signals indicating data stored within the data store and
4 the FIFO.

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1 16. The UART of Claim 11 wherein the processing means
2 further indicates whether a receiver enable flag has been
3 set.

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1 17. The UART of Claim 16 wherein the receiver control
2 circuitry further enables and disables the receiver in
3 response to the indication of whether the receiver enable
4 flag has been set.

1 18. An apparatus for providing single channel
2 communications, comprising:
3 a transmitter for transmitting signals on a
4 single communications channel;
5 a receiver for receiving signals on the single
6 communications channel;

7 processing means for generating an indication of
8 at least one of whether the apparatus is in a full-duplex
9 mode and whether the receiver enable flag is set;
10 means for storing data to be transmitted by the
11 transmitter, the means for storing generating an indication
12 when data for transmission is contained therein; and
13 receiver control circuitry for disabling and
14 enabling the receiver in response to at least one of the
15 indications of whether the apparatus is in a full duplex
16 mode, whether the receiver enable flag is set and whether
17 the means for storing contains data for transmission.

1 ⁸
2 ~~19~~. The apparatus of Claim ¹~~18~~ wherein the means for
3 storing further comprises:

4 a data store for queuing data to be transmitted
5 by the transmitter; and
6 a FIFO for passing data between the data store
7 and the transmitter.

1 ⁹
2 ~~20~~. The apparatus of Claim ⁸~~19~~ wherein the receiver
3 control circuitry disables and enables the receiver in
4 response to signals indicating data within the data store
5 and the FIFO.

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1 21. A control circuit for a UART, comprising:

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means for receiving at least one of a first
3 indicator of whether the UART operates in a full-duplex
4 mode, a second indicator of whether a receiver flag is set,
5 a third indicator of whether data for transmission is
6 stored; and
7 control circuitry for disabling a receiver
8 responsive to at least one of the first, second and third
9 indicators.

REMARKS

In response to the Office Action dated June 2, 1997, the Applicant has deleted the present title and added new title "Method and Apparatus for Providing Single Channel Communications" and has deleted Claims 1-10 and added new Claims 11-21. Thus, Claims 11-21 are now pending in the application. Reconsideration of the Claims, as amended, is respectfully requested.

The Examiner objected to the title of the invention as being nondescriptive. The Applicant has cancelled the existing title and provided the title "Method and Apparatus for Providing Single Channel Communications". Applicant submits that this is descriptive of the invention and respectfully requests withdrawal of the Examiner's objection.

The Examiner rejected Claims 1-10 under 35 U.S.C. §102(b), as being anticipated by Paneth, et al. As these